



EL 844051935

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#15
Vishnu
10/17/01

PATENT APPLICATION SERIAL NO. 09/512,149
FILING DATE February 23, 2000
INVENTORSHIP Vishnu K. Agarwal
GROUP ART UNIT 2814
EXAMINER M. Pizarro Crespo
ATTORNEY'S DOCKET NO. MI22-1322
TITLE.....Integrated Circuitry Including a Capacitor With an Amorphous and a
Crystalline High K Capacitor Dielectric Region (As Amended)

Attention: Official Draftsman
Assistant Commissioner for Patents
Washington, D. C. 20231

LETTER SUBMITTING FORMAL DRAWINGS

Please enter the enclosed formal drawing 5/5, Fig. 9, in the above-referenced application in place of the originally filed drawing 5/5, Fig. 9.

This formal sheet is being submitted to correct 5/5, Fig. 9. The proposed changes are indicated in red on attached copies. The formal drawing incorporate the proposed changes.

The drawings were objected to by the Examiner in the June 20, 2001 Office Action for Application No. 09/797,899, mailed on June 20, 2001, which is a divisional for the above application.

Acknowledgment of receipt of the formal drawing sheet and their acceptance into the file is requested.

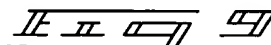
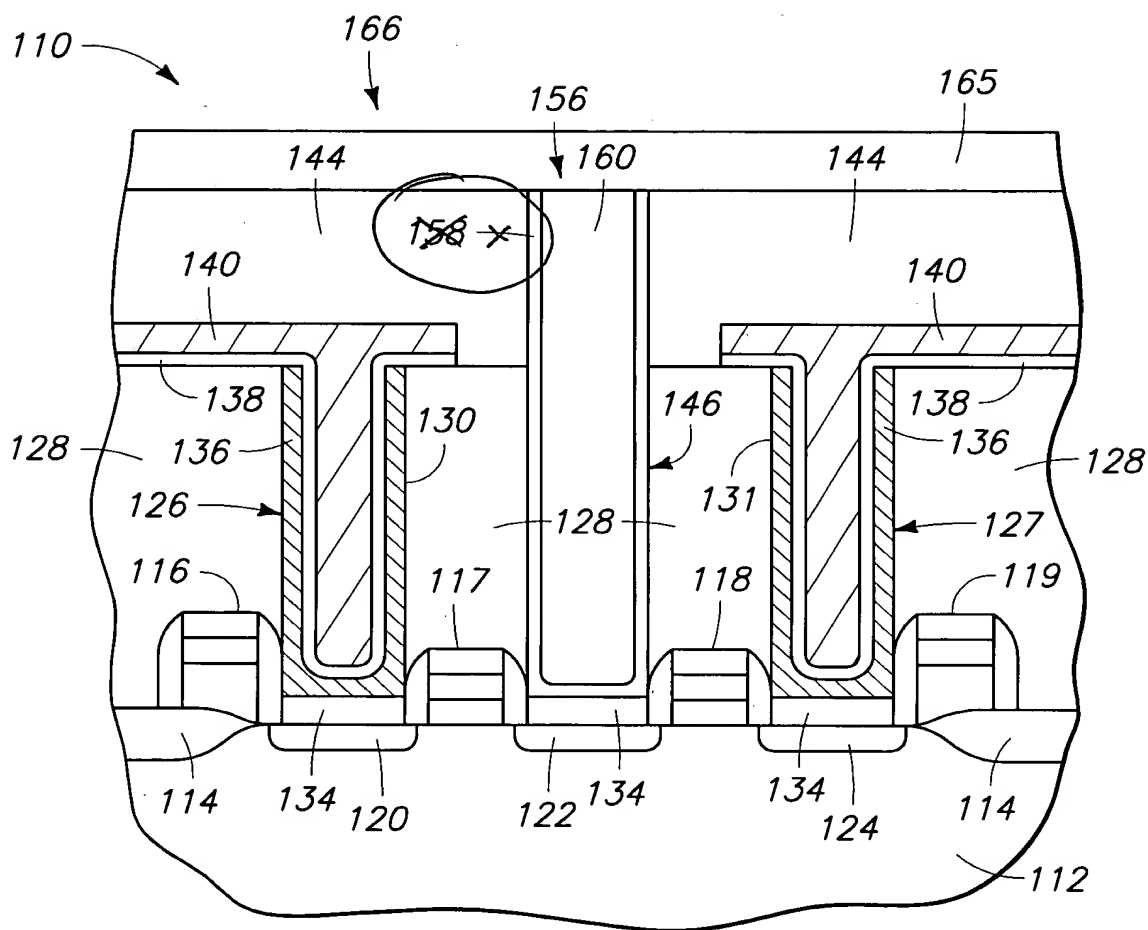
Respectfully submitted,

Date: Oct 3, 2001

By: Bernard Berman

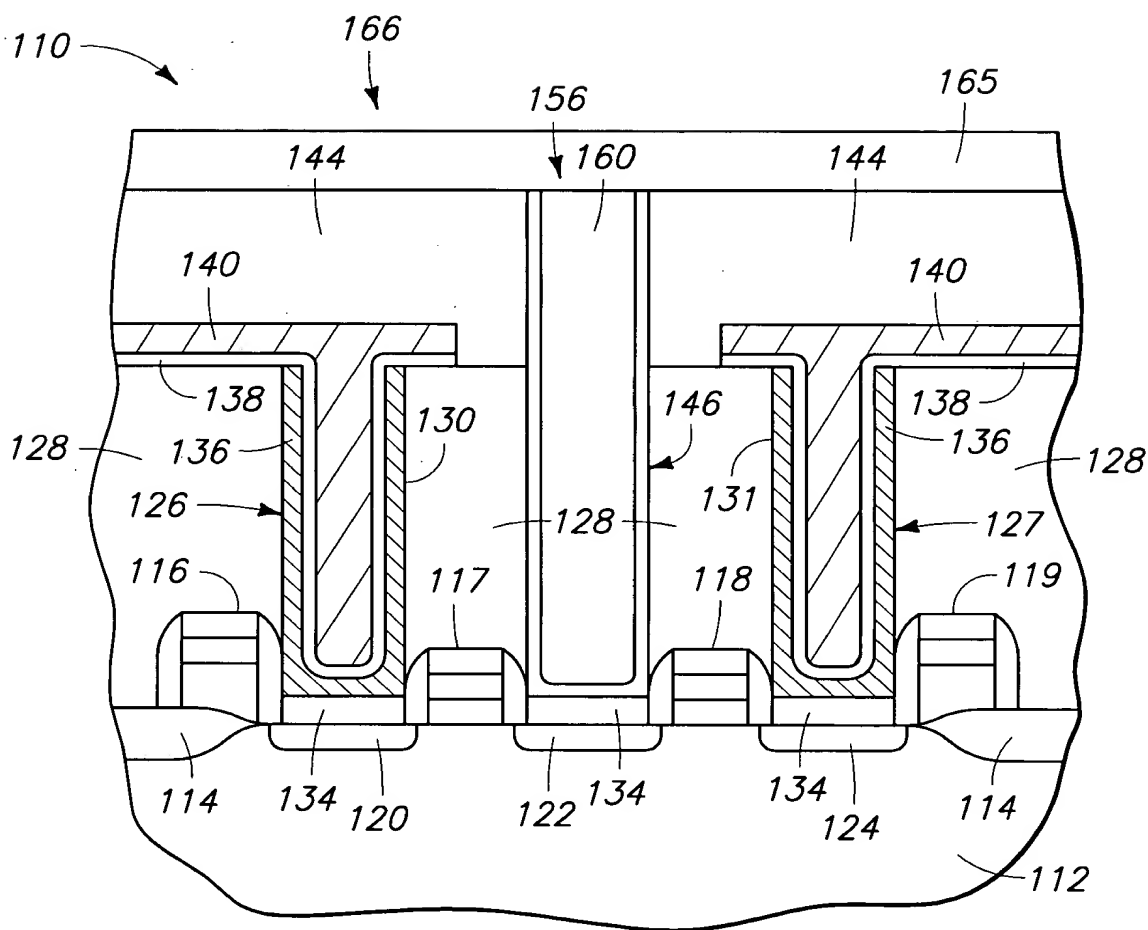
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Enclosures: Corrected Drawing 5/5, Fig. 9; Red-inked Drawing 5/5, Fig. 9.



"APPROVED"
MSP
11/29/2001

5/5



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